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APPLICATION NO. FILING DATE FIRST NAMED INVENTOR ATTORNEY DOCKET NO. 09/690,580 10/17/00 VASHCHENKO V NSC1-H1500 **EXAMINER** MM91/1024 MARK C. PICKERING FARAHANI PILLSBURY WINTHROP LLP **ART UNIT** PAPER NUMBER 50 FREMONT STREET, FIFTH FLOOR SAN FRANCISCO CA 94105-2230 2814 **DATE MAILED:**

Please find below and/or attached an Office communication concerning this application or proceeding.

Commissioner of Patents and Trademarks

10/24/01

	Application No.	Applicant(s)
Office Action Summary	09/690,580	VASHCHENKO ET AL.
	Examiner	Art Unit
The MAN WOOD ATT	Dana Farahani	2814
The MAILING DATE of this communic Period for Reply	ation appears on the cover sheet with	h the correspondence address
A SHORTENED STATUTORY PERIOD FO THE MAILING DATE OF THIS COMMUNIC - Extensions of time may be available under the provisions of after SIX (6) MONTHS from the mailing date of this commun - If the period for reply specified above is less than thirty (30) - If NO period for reply is specified above, the maximum statu - Failure to reply within the set or extended period for reply wi - Any reply received by the Office later than three months afte earned patent term adjustment. See 37 CFR 1.704(b).	ATION. 37 CFR 1.136(a). In no event, however, may a reprication. days, a reply within the statutory minimum of thirty (tory period will apply and will expire SIX (6) MONTH.	oly be timely filed (30) days will be considered timely. HS from the mailing date of this communication.
Status		
1) Responsive to communication(s) filed	d on <u>10 October 2000</u> .	
	n)⊠ This action is non-final.	
3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.		
Disposition of Claims		,
4)⊠ Claim(s) <u>1-8</u> is/are pending in the application.		
4a) Of the above claim(s) is/are withdrawn from consideration.		
5) Claim(s) is/are allowed.		
6)⊠ Claim(s) <u>1-8</u> is/are rejected.		
7) Claim(s) is/are objected to.		
8) Claim(s) are subject to restrictio	n and/or election requirement.	
Application Papers	•	
9)☐ The specification is objected to by the E	xaminer.	
10) ☐ The drawing(s) filed on is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.		
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).		
11) The proposed drawing correction filed on is: a) approved b) disapproved by the Examiner.		
If approved, corrected drawings are required in reply to this Office action.		
12) The oath or declaration is objected to by the Examiner.		
Priority under 35 U.S.C. §§ 119 and 120		
13) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).		
a) ☐ All b) ☐ Some * c) ☐ None of:		
 Certified copies of the priority documents have been received. 		
2. Certified copies of the priority documents have been received in Application No		
Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received.		
14) Acknowledgment is made of a claim for d	omestic priority under 35 U.S.C. & 1	19(e) (to a provisional application)
a) The translation of the foreign langua	age provisional application has been	received
Attachment(s)		·- ··
1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-93) Information Disclosure Statement(s) (PTO-1449) Paper	348) 5) Notice of Inform	mary (PTO-413) Paper No(s) mal Patent Application (PTO-152)
.S. Patent and Trademark Office PTO-326 (Rev. 04-01)	ffice Action Summary	Part of Paper No. 5

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DETAILED ACTION

Drawings

The drawings are objected to because they are not suitable for reproduction.
 Correction is required.

Claim Rejections - 35 USC § 103

- 2. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 3. Claims1 and 6 are rejected under 35 U.S.C. 103(a) as being unpatentable over Lowrey (U.S. 5581104) et al. in view of Delage (U.S. 6031255) et al.

Lowrey discloses a bipolar transistor-based ESD protection structure comprising an N-type semiconductor substrate, figure 8, a bipolar transistor disposed in and on the semiconductor substrate, the bipolar transistor having a base region, shown as an N region, a collector region, shown as a P+ region connected to an input, and a polysilicon emitter P+ connected to Vcc. However Lowrey does not disclose a heat sink. Delage, in figure 9, discloses a heat sink region 101, integrated with the metal layer 94, adjacent to the emitter 40 and above the semiconductor substrate 10, in order to reduce the heat generated during an ESD event. Therefore it would have been obvious to one of

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ordinary skill in the art at the time of the invention to use the heat sink Delage discloses in the Lowrey invention in order to reduce the heat generated.

- 4. Claim 2 is rejected under 35 U.S.C. 103(a) as being unpatentable over Lowrey and Delage as applied to claim 1, above, and further in view of Leuschner (U.S. 4724471). Delage, as applied to Lowrey, does not disclose the heat sink is formed of polysilicon. However, it is well known in the art that polysilicon layers are commonly used as heat sinks (see for example Leuschner, column 8, lines16-20). Therefore, it would have been obvious to one of ordinary skill in the art at the time of the invention to form the heat sink Delage discloses from polysilicon in order to make a good heat sink.
- 5. Claims 3-5 are rejected under 35 U.S.C. 103(a) as being unpatentable over Lowrey and Delage as applied to claim 1, above, and further in view of Akram (U.S. 6075288).

Regarding claims 3 and 4, Delage, as applied to Lowrey, does not disclose the heat sink region formed of metal layers. Akram describes a floating heat sink formed of copper, aluminum, molybdenum, titanium, and alloys of these materials. (see column 3, lines 40-44). Therefore, it would have been obvious to one of ordinary skill in the art at the time the invention was made to form the heat sink Delage discloses from the material Akram describes in order to form a heat sink with a high thermal conductivity.

Regarding claim 5, Akram, as applied to claims 3 and 4 above, does not disclose the heat sink is within 2 microns of the polysilicon emitter. However, it would have been

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an obvious matter of design choice to those skilled in the art to dispose the heat sink region within 2 microns of the polysilicon emitter since heat transfer by conduction is more efficient.

6. Claims 7 and 8 are rejected as being unpatentable over Lowrey and Delage as applied to claims 1 above and further in view of applicant's Admitted Prior Art. Lowrey does not include a Zener Triggered bipolar transistor-based ESD protection and a grounded base configuration bipolar transistor. However, in the applicant's Admitted Prior Art (see page 2, line 21), the applicant has disclosed that these configurations are commonly used in the art for transistor based ESD structures. Therefore it would have been obvious to one of ordinary skills in the art at the time the invention was made to include a grounded base configuration bipolar transistor and a Zener Triggered bipolar transistor in the Lowrey invention in order to conduct the ESD current to a substrate or ground.

Conclusion

- 7. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.
- 8. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Dana Farahani whose telephone number is (703)305-1914. The examiner can normally be reached on M-F 9:00AM 6:00PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Olik Chaudhuri can be reached on (703)306-2794. The fax phone numbers for the organization where this application or proceeding is assigned are (703)305-3432 for regular communications and (703)305-3432 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703)308-0956.

Dana Farahani October 16, 2001

OLIK CHAUDHURI
SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2800